

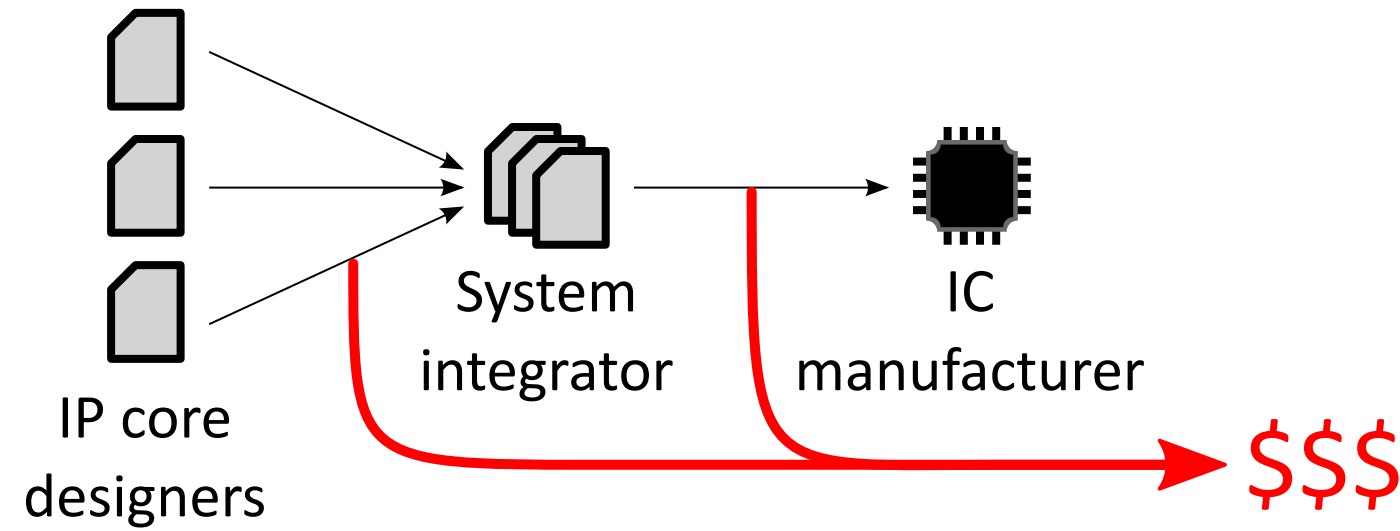
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Current situation

- Integrated circuits are increasingly **complex**,
- **Core-based** design is the norm,
- Design data **transfer** is **necessary**,
- **Illegal copying** and **overusing** are rising.



Proposed solution

Make the design **remotely activable**.

Requirements:

- **Unique** identifier for each instance,
- Two modes of operation (**disabled** and normal),
- **Mid-term** security.

Characteristics:

- **Lightweight**,
- **Easy** to integrate into EDA tools,
- Usable with **all types** of designs.

Implementation results

	6-LUTs	DFFs
PUF	4841	32
Controller	104	90
MUXes (parities & indexes)	338	0
Parity computation module	1	1
AW sampler	0	128
Parity shift register	0	32
Response shift register	0	128

