



Problem

Once the design is provided to the foundry, the designer cannot control **how many** copies are produced.

~10% of the electronics market is counterfeit (>\$20 billion)

Proposed solution

- The design is modified to make the produced circuit **locked**.
- After production, each circuit must be **individually unlocked** by the original designer who sends a **unique activation key**.

Make the design lockable

Gears

Logic gates

□ Existing parts
 ■ Added, controllable parts
 ■ Controller

Force the outputs to a fixed value

Insertion algorithm:

Results:

- Only 3% area overhead on average,
- 10000x faster than existing insertion algorithms.

Make the design uniquely identifiable

Fingerprint for twins

PUFs for ICs

Extract a unique ID from random physical variations

Integrated circuit

Designer's database

$ID_0 \leftarrow \text{PUF}$ → Enrolment → ID_0 → Stores ID_0

... Activation

$ID_i \leftarrow \text{PUF}$ → ID_i →

- If $ID_i = ID_0$ → [IC with checkmark]
- If $ID_i \neq ID_0$ → [IC with X]

Circuit identification